

NANO FPGA

Technical Specification:

- Santa Cruz connector for docking FPGA card.
- 8 bit input Dip switch and 8 output Leds.
- Xilinx FPGA XC3S250E-4PQ208.
- 72 Microcells with 8MHZ Oscillator.
- USB JTAG connector.
- User Manual.
- Sample code maximum user I/O 32 for Xilinx FPGA.
- ON board USB mode Programming Facility.



All The Technology You need

ISO 9001:2008
Certified Company



ADVANCE TECH INDIA PVT. LTD

Website: www.atechindia.com. E-mail: atechindia@gmail.com