

ADVANCED DIGITAL EXPERIMENT SYSTEM

Features

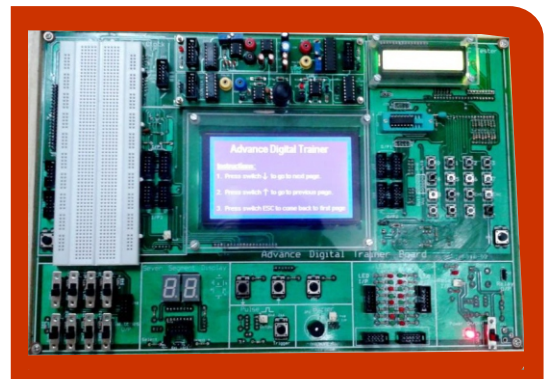
- VLSI based backend design module,
- Attractive graphical LCD.
- Graphical integrated chip Experiments images.
- Onboard IC tester.
- Timer, counter, PWM based dc motor control.

Technical Specification

- The system is based around combination of VLSI CPLD chip and Microcontroller.
- The system should be virtual present of all digital ICs along with their IC diagram on 240 x 128 graphical LCD.
- The facility to connect diagram Input & ,Output to virtual selected
- IC and show output on the LCD study the output response on LED's
- Digital logic Integrated circuits (IC) testing facility using digital IC tester. Input 4x4 hex keypad.
- On board signal generator from 1 Hz to 700khz. IC devices for experimentation
- On board buzzer, Various lists of Digital experiments must be performed, bread board facility to implement external circuits,
- On board relay section,7-segment section 2Nos, combinational logic, sequential logic, ADC circuit to conversion.
- DAC experiment circuits to conversion. Timers and counters circuits.
- The system is based around combination of VLSI CPLD chip and Microcontroller.
- PWM based 5v dc motor speed control
- Accessories included : Mains cord, operating and experimental manual, connecting lead, Power: 220V±5%, 50Hz.

Experiments

- Basic Logic Gate Practice.
- Combinational Logic Circuit Practice.
- Arithmetic Circuit Practice.
- Multiplexer and De-multiplexer circuit practice
- Flip-flop circuit Practice.
- Multivibrator Circuit Practice
- Timer circuit practice
- Counter circuit practice
- PWM circuit practice.
- understand the usage of a relay



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