

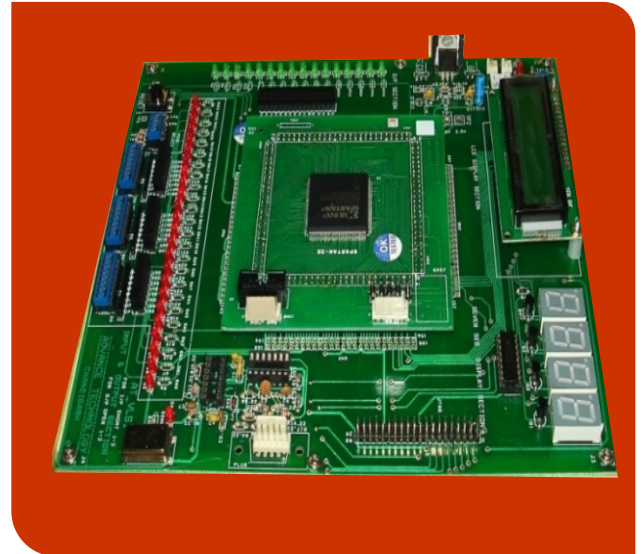
Universal FPGA/ CPLD Development Board

Technical specification

- Universal platform For Both CPLD and FPGA
- Support FPGA spartan 3E Daughter Card
- CPLD 9572 daughter Card
- On Board JTAG Programming Section
- On Board Power supply.
- On Board 24 i/p DIP switch With LED Indicator
- 16 O/p LED Indicator
- On Board LCD(16X2) section
- On Board 4 seven segment Display.
- On Board Oscillator section .
- 40 Pin I/O connector to Interface external Device

Experiments

- Design Of Basic Gates
- Design Of XOR Gate Using Other Basic Gate
- Design Of 2:1 MUX
- Design Of 2:4 Decoder
- Design Of Adders And Subtractors
- Design Of 3:8 Decoder
- Design Of 8:3 Priority Encoder
- Design Of 4 Bit Binary To Gray Code Converter
- Design Of 4-Bit Binary To BCD Converter
- Design Of Parity Generator
- Design Of 8 Bit Binary 2ⁿ complementer
- Design Of Flip Flops
- Design Of 8 Bit Shift Register
- Design Of Synchronous 8-Bit Johnson Counter
- Design Of Universal Shift Register (PIPO)
- Design of 4-bit binary to BCD Converter Using Sequential Statement
- Design Of Counters
- UP/DOWN COUNTER(00 TO 99)
- Program to display data on LCD
- Program to display data on Seven segment
- Design Of 3:8 counter
- Design of ALU.
- Many Interface Unit Can Interface with this Board Like –ADC,DAC,Traffic Light
- VGA,Motors,PS2 Keyboard ,Wireless-GSM,Bluetooth,RFID etc .



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ADVANCE TECH INDIA PVT. LTD

Website: www.atechindia.com. E-mail: atechindia@gmail.com