

CDMA Trainer Kit

Introduction

CDMA Trainer kit comprises of detailed process of CDMA techniques of sending any information with security. One will get the knowledge of the basic mechanism used in the CDMA. The kit comprises of CDMA-DSSS (Direct Sequence Spread Spectrum) and CDMA-FHSS (Frequency Hopping Spread Spectrum) along with slow/fast hopping feature. One can also learn about BPSK Modulator and Demodulator and DSSS generation and reception.

Key Features

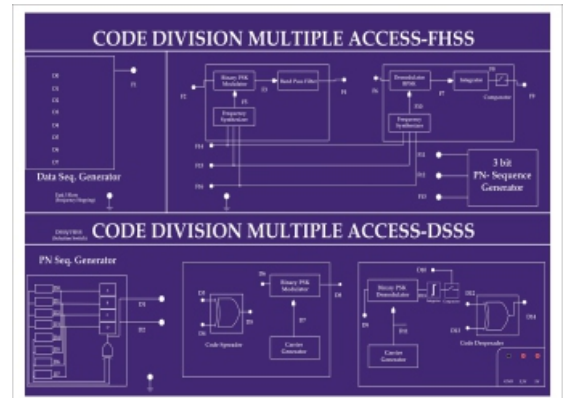
- CDMA Direct Sequence Spread Spectrum (Generation and Reception)
- CDMA Frequency Hopping Spread Spectrum (Generation and Reception)
- Slow Hopping and Fast Hopping Concept in CDMA-FHSS
- BPSK Modulation & Demodulation
- Direct Sequence Spread Spectrum Encoding and Decoding

Technical Specification

- Concept circuit diagrams are printed on board.
- Technique Selection Sliding Switch Slow /Fast Selection Sliding Switches.
- LED Indication for Data and PN Sequence switches.
- Test point facility to measure & observe output signals.
- Consist of 19 Test Points.
- FPGA based Implementation of the CDMA Techniques.
- Based on Xilinx Spartan 3E FPGA
- 8MB SPI Flash for FPGA reconfiguration
- Power supply with +3.3V,+ 5V
- 8MHz Crystal oscillator.

Experiments

- Study of Direct Sequence Spread Spectrum Modulation and Demodulation Process.
- Study of BPSK Modulation and Demodulation Process.
- Study of Code Division Multiple Access (DSSS) Technique.
- Study of Code Division Multiple Access (FHSS) Modulation and Demodulation



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